# LD5523E2 test report

## Efficiency Measurement

115V			230V				
Vout(V)	Iout(A)	Pin(W)	Efficiency	Vout(V)	Iout(A)	Pin(W)	Efficiency
12.02	2.0	28.10	85.55	12.15	2.0	27.85	87.25
11.99	1.5	20.87	86.18	12.05	1.5	20.66	87.48
11.98	1.0	13.63	87.89	11.95	1.0	13.65	87.54
11.91	0.5	6.77	87.96	11.88	0.5	6.83	86.96
		Avg.	86.90%			Avg.	87.30%
11.95	0.2	2.78	85.97%	11.92	0.2	2.89	82.49

Power saving 64mW@230Vac

> NOTE : CoC V5 Tier-2 spec: 12V Avg. Eff. >86.8%, 10% Eff. > 76.20%

➤ NOTE: 1.5M /20AWG, NTC 5R/3A

## Output Ripple Voltage & Noise

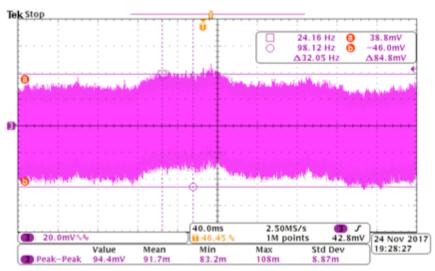
Input	12V/2A ripple noise	Spec <120mV	
90VAC / 60Hz	108mV	Pass	
264VAC / 50Hz	94mV	Pass	

#### 90Vac/2A

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CH3: Vop-p(ac)

264Vac/2A



CH3: Vop-p(ac)

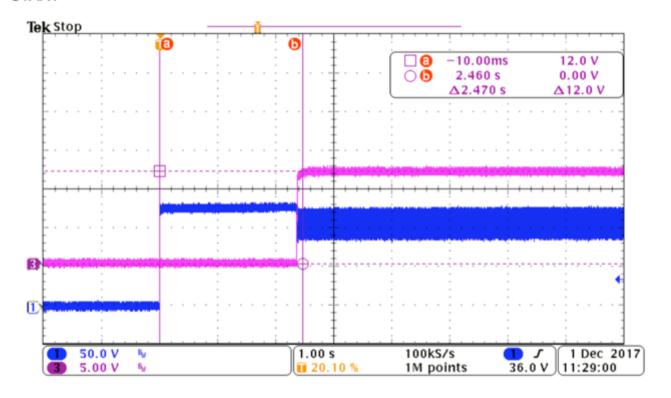
### **Over Current Protection**

Input	12V I <sub>o</sub> (Max)	Spec. tolerance ± 6%	
90VAC / 60Hz	2.64A		
115VAC / 60Hz	2.60A	+ 2 020/	
230VAC / 50Hz	2.58A	± 2.92%	
264VAC / 50Hz	2.49A		

## Turn On Delay Time

Input	Turn on delay time	Spec
90VAC / 60Hz	2.47 Sec	< 3 sec

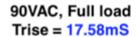
R<sub>START</sub>: 510Kohm\*2

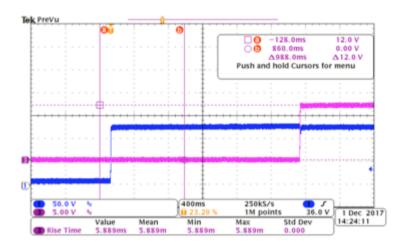


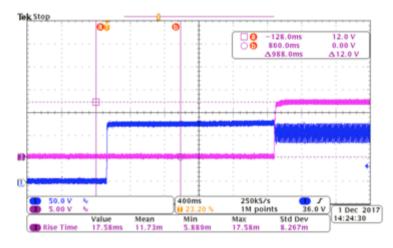
CH1: Vout, CH2: Bulk voltage

## **Output Rise Time**

90VAC, No load Trise =5.88 mS

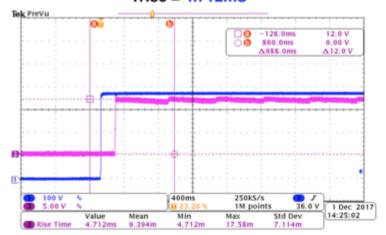


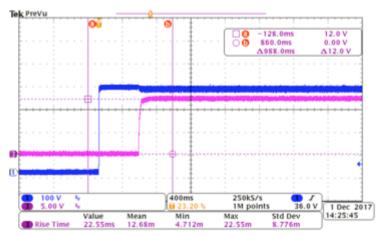




264VAC, No load Trise = 4.712mS

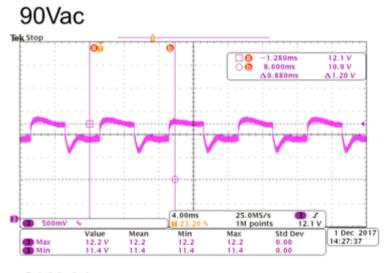




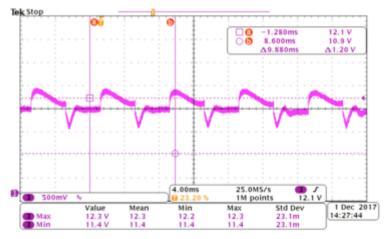


## **Dynamic Load Response**

T	0 4 1 1	Rea		
Input	Output Dynamic	Vmax(V)	Vmin(V)	+/-%
90VAC / 60Hz	0.2A <b>→</b> 1.8A	12.2	11.4	± 3.39%
264VAC / 50Hz	0.2A <b>→</b> 1.8A	12.3	11.4	± 3.8%
SPEC	12V ±1.2V			
Result		PASS		



#### 264Vac



CH<sub>3</sub> Vo

CH3 Vo