











AFE4900

SBAS861 - AUGUST 2017

# AFE4900 Ultra-Low Power, Integrated AFE for Wearable Optical, Electrical Bio-Sensing With FIFO

#### **Features**

- Synchronized PPG, ECG Signal Acquisition at Data Rates Up to 1 kHz
- ECG Signal Chain:
  - Standalone ECG Acquisition Up to 4 kHz
  - Input Bias: 1-Lead ECG With RLD Bias
  - Programmable INA Gain: 2 to 12
  - Input Noise (1 Hz to 150 Hz): 2.5 µVrms at 1 kHz, 1.25 µVrms at 4-kHz Data Rate
  - AC, DC Lead-Off Detect: 12.5-nA to 100-nA
- PPG Receiver:
  - Supports Three Time-Multiplexed PD Inputs
  - 24-Bit Representation of Current From PD
  - DC Offset Subtraction DAC (Up to ±126-μA) at TIA Input for Each LED, Ambient
  - Digital Ambient Subtraction at ADC Output
  - Noise Filtering With Programmable Bandwidth
  - Transimpedance Gain: 10 k $\Omega$  to 2 M $\Omega$
  - Dynamic Range Up to 100 dB
  - Receiver Operates in PPG-Only mode at Approximately 1-µA/Hz Sampling Rate
  - Power-Down Mode: Approximately 0 μA
- **PPG Transmitter:** 
  - Four LEDs in Common Anode Configuration
  - 8-Bit LED Current Up to 200 mA
  - Mode to Fire Two LEDs in Parallel
  - Programmable LED On-Time
  - Simultaneous Support of Three LEDs for SpO<sub>2</sub>, or Multiwavelength HRM
  - Average Current of 30 μA Adequate for a Typical Heart-Rate Monitoring Scenario:
    - 20-mA Setting, 60-μs Pulse Duration, 25-Hz Sampling Rate
- Clocking Using an External or Internal Clock
- FIFO With 128-Sample Depth for ECG and PPG
- I<sup>2</sup>C, SPI<sup>™</sup> Interfaces: Selectable by Pin
- Operating Temperature Range: -20°C to +70°C
- 2.6-mm × 2.1-mm, 0.4-mm Pitch DSBGA Package
- Supplies:
  - Rx: 1.8 V to 1.9 V (LDO Bypass), 2.0 V to 3.6 V (LDO Enabled)
  - Tx: 3 V to 5.25 V
  - IO: 1.7 V to Rx\_SUP

### 2 Applications

- Synchronized PPG, ECG for Blood Pressure Estimation
- HRM for Wearables, Hearables
- Heart-Rate Variability (HRV)
- Pulse Oximetry (SpO<sub>2</sub>) Measurements

#### 3 Description

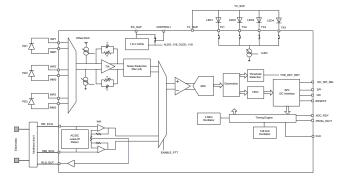
The AFE4900 device is an analog front-end (AFE) for electrocardiogram synchronized photoplethysmogram (PPG) signal acquisition. The device can also be used for optical bio-sensing applications, such as heart-rate monitoring (HRM) and saturation of peripheral capillary oxygen (SpO<sub>2</sub>). The PPG signal chain supports up to four switching light-emitting diodes (LEDs) and up to three photodiodes (PDs). The LEDs can be switched on using a fully integrated LED driver. The current from the photodiode is converted into voltage by the transimpedance amplifier (TIA) and digitized using an analog-to-digital converter (ADC). The ECG signal chain has an instrumentation amplifier (INA) with a programmable gain that interfaces to the same ADC. A right-leg drive (RLD) amplifier set can be used to the bias for the ECG input pins. AC and dc lead-off detect schemes are supported. The ADC codes from the PPG and ECG phases can be stored in a 128sample first in, first out (FIFO) block and read out using either an I<sup>2</sup>C or a serial programming interface (SPI) interface.

#### Device Information<sup>(1)</sup>

PART NUMBER	PACKAGE	BODY SIZE (NOM)				
AFE4900	DSBGA (30)	2.60 mm × 2.10 mm				

(1) For all available packages, see the package option addendum at the end of the datasheet.

#### Simplified Block Diagram



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# TEXAS INSTRUMENTS

### 4 Device and Documentation Support

#### 4.1 Trademarks

All trademarks are the property of their respective owners.

#### 4.2 Electrostatic Discharge Caution



This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

### 4.3 Glossary

SLYZ022 — TI Glossary.

This glossary lists and explains terms, acronyms, and definitions.

# 5 Mechanical, Packaging, and Orderable Information

The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.

Product Folder Links: AFE4900



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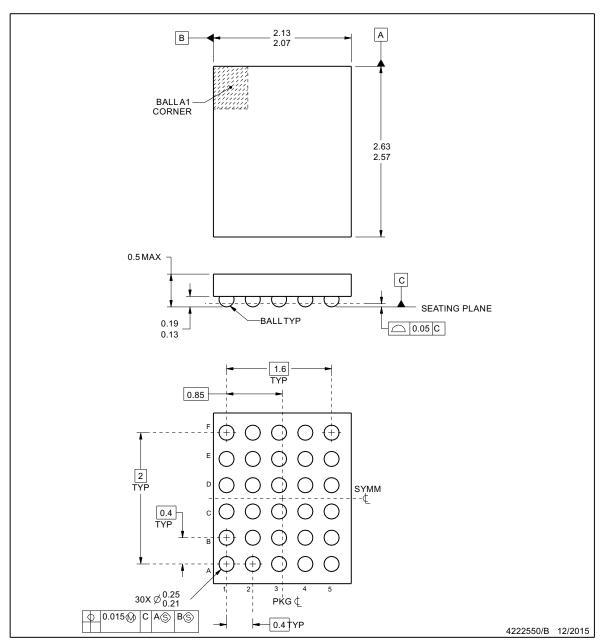
YZ0030-C01



#### PACKAGE OUTLINE

# DSBGA - 0.5 mm max height

DIE SIZE BALL GRIDARRAY



#### NOTES:

- 1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.

  2. This drawing is subject to change without notice.

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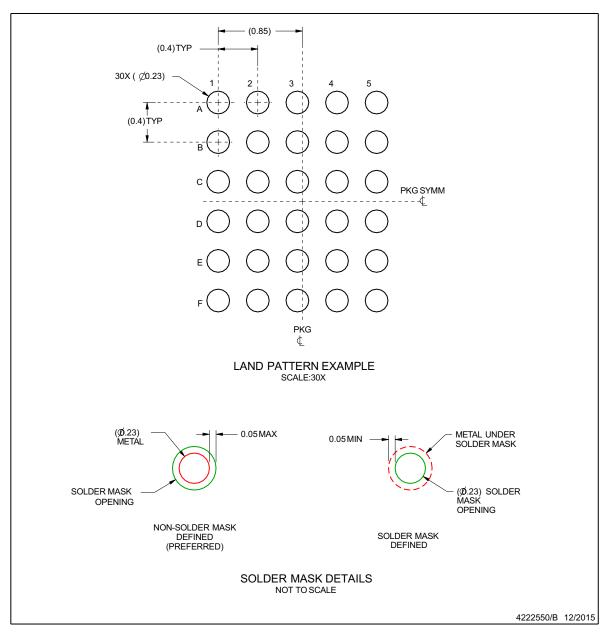


# YZ0030-C01

# **EXAMPLE BOARD LAYOUT**

DSBGA - 0.5 mm max height

DIE SIZE BALL GRID ARRAY



NOTES: (continued)

3. Final dimensions may vary due to manufacturing tolerance considerations and also routing constraints. See Texas Instruments Literature No. SNVA009 (<a href="https://www.ti.com/lit/snva009">www.ti.com/lit/snva009</a>).

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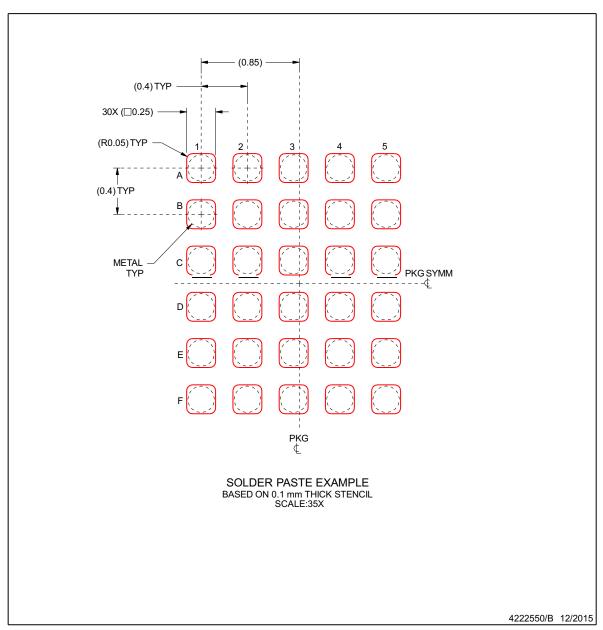
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# YZ0030-C01

# **EXAMPLE STENCIL DESIGN**

# DSBGA - 0.5 mm max height

DIE SIZE BALL GRIDARRAY



NOTES: (continued)

4. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release.

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## PACKAGE OPTION ADDENDUM

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#### PACKAGING INFORMATION

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Orderable Device	Status	Package Type	Package Drawing	Pins	Package Qty	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking	Samples
AFE4900YZR	ACTIVE	DSBGA	YZ	30	3000	Green (RoHS & no Sb/Br)	SAC396	Level-1-260C-UNLIM	-20 to 70	AFE4900	Samples
AFE4900YZT	ACTIVE	DSBGA	YZ	30	250	Green (RoHS & no Sb/Br)	SAC396	Level-1-260C-UNLIM	-20 to 70	AFE4900	Samples

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) RoHS: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

**Green:** TI defines "Green" to mean the content of Chlorine (CI) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

- (3) MSL, Peak Temp. The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.
- (4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.
- (5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.
- (6) Lead/Ball Finish Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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# PACKAGE MATERIALS INFORMATION

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# TAPE AND REEL INFORMATION





	Dimension designed to accommodate the component width
	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



#### \*All dimensions are nominal

Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
AFE4900YZR	DSBGA	YZ	30	3000	178.0	9.2	2.27	2.72	0.78	4.0	8.0	Q1
AFE4900YZT	DSBGA	YZ	30	250	178.0	9.2	2.27	2.72	0.78	4.0	8.0	Q1

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#### \*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
AFE4900YZR	DSBGA	YZ	30	3000	220.0	220.0	35.0
AFE4900YZT	DSBGA	YZ	30	250	220.0	220.0	35.0

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